

Priority No : ES200203037

03/16/04

DOCKET NO: Lear

DECLARATION & POWER OF ATTORNEY

As a below named inventor, I/we hereby declare that:

My/Our residences and citizenships are as listed below.

I/We believe that I/we are the original, first and sole/joint inventor(s) of the subject matter which is claimed and for which a patent is sought in the invention entitled **Process for manufacturing printed circuit boards with protected spaces between tracks**, identified as Lear attorney docket number **04859**, the specification of which is filed concurrently herewith.

I/We hereby state that I/we have reviewed and understand the contents of the above identified specification including the claims, as amended by any amendment specifically referred to in this Declaration.

I/We acknowledge the duty to disclose information that is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

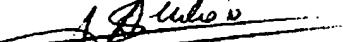
I/We hereby appoint Bruce E. Harang, Registration No. 29,720; Bill C. Panagos, Registration No. 31,050; and Larry Shelton, Registration No. 45,100 to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith. Please send all correspondence to:

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I/We hereby declare that all statements made herein of my/our own knowledge are true, and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements and the like so made jeopardize the validity of the application or any patent issued thereon.

Inventor's name: Jose Antonio Cubero Pitel

Citizenship: Spain

Signature: 

Date: 7-1-2004

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